



PATENT

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Electron
Immunology
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IN THE UNITED STATES PATENT OFFICE

Applicants: Jeffrey M. Mason et al.
Assignee: Xilinx, Inc.
Title: "Modular Design Method and System for Programmable Logic Devices"

Serial No.: 09/839,464 Filed: 04-20-01
Examiner: Douglas W. Owens Art Unit: 2811
Docket No.: X-658 US

Mail Stop NON-FEE AMENDMENT
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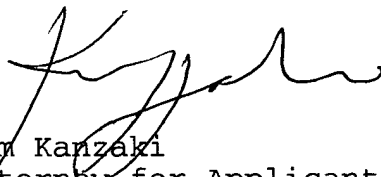
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RESPONSE TO RESTRICTION REQUIREMENT

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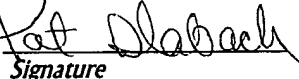
In response to the Restriction Requirement mailed from the Patent Office on June 3, 2003, Applicants elect without traverse to prosecute Group I: Claims 1-71 and 76-79. Applicants hereby cancel Claims 72-75 (Group II) but reserve the right to prosecute Group II (Claims 72-75) in a subsequent divisional application.

Respectfully submitted,


Kim Kanzaki
Attorney for Applicants
Reg. No. 37,652

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Pat Slaback
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Modified 02-03

PTO/SB/21 (01-03)
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application / Conf. No.	09/839,464 / 2465
	Filing Date	April 20, 2001
	First Named Inventor	Jeffrey M. Mason
	Examiner Name	Douglas W. Owens
	Art Unit	2811
	Patent No.	
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<input type="checkbox"/> Preliminary Amendment	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)
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Date	July 1, 2003	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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